IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Vishnu K. Agarwal et al.

Title:

TRANSISTOR ATTRIBUTES

303.780US1

Filed:

Docket No.:

January 17, 2002

Examiner:

Hoai Pham

STRUCTURE HAVING

Serial No.: 10/053300

REDUCED

TRANSISTOR

Due Date: N/A

Group Art Unit: 2814

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

- An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.
- A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 312 day of March, 2004.

Name

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

<u>S/N 10/053300</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vishnu K. Agarwal et al. Examiner: Hoai Pham

Serial No.: 10/053300 Group Art Unit: 2814

ed: January 17, 2002 Docket: 303.780US1

Re: TRANSISTOR STRUCTURE HAVING REDUCED TRANSISTOR LEAKAGE

ATTRIBUTES



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

04/07/2004 MDAMTE1 00000139 10053300

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INFORMATION DISCLOSURE STATEMENT

Serial No :10/053300

Filing Date: January 17, 2002

Title: TRANSISTOR STRUCTURE HAVING REDUCED TRANSISTOR LEAKAGE ATTRIBUTES

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

VISHNU K. AGARWAL ET AL.

By their Representatives,

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Minneapolis, MN 55402
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Date Mar. 31, 2004

By <u>Jan</u>

Reg. No. 32,146

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Nome

Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT

PTO/SB/08A(10-01)
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known 10/053300 **Application Number** January 17, 2002 **Filing Date** Agarwal, Vishnu **First Named Inventor Group Art Unit** 2814 Pham, Hoai **Examiner Name**

as many sheets as necessary)

Attorney Docket No: 303.780US1

	US PATENT DOCUMENTS									
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate				
	US-5,223,730	06/29/1993	Rhodes, Howard E., et al.	257	301	02/03/1992				
	US-5,405,788	04/11/1995	Manning, Monte, et al.	438	241	05/24/1993				
	US-5,661,045	08/26/1997	Manning, Monte, et al.	438	286	12/09/1996				
	US-6,630,706	10/07/2003	Yang, Rongsheng, et al.	257	310	08/30/2001				

FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²			

	OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS						
Examiner	Cite	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item	T²				
Initials*	No 1	(book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s),					
1	1	publisher, city and/or country where published.					

EXAMINER